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1. PLC-1 Specification
... to build **low**-cost, **high**-performance PLC networks ... and LTRINT **configuration bits**. HSK. **Tri-state** output, Hands Input, CS Polarity: 0: **active low**, 1: **active high**. ...
www.qbc.clic.net/~metricom/spec1.html - 67k - Cached - More pages from this site

2. SEE - cosmic rays **and high** energy protons
... device **active** volume and pass ... a very **low** heavy ion ... **high**-current **state** which may exceed current density requ reliable operation. Bus fights on internal **tri** ...
www.phys.ualberta.ca/~gingrich/atlas/radiation/SEEnotes.html - 13k - Cached - More pages from this site

3. STMicroelectronics | 48-96 KBYTE ROM HCMOS MCU WITH ON-SCREEN DISPLAY AND TELET SLICER
STMicroelectronics is a global leader in developing and delivering System-on-Chip (SoC) and semiconductor solutions spectrum of microelectronics applications: communications, computer ... Up to 8 Kbytes **TRI** TDSRAM MEMORY BUS I Reset (input, **active low**). The ST9+ is initialised ... Control Watchdog Timer **High** Register Watchdog Timer **Low** Regis Timer ...
www.st.com/stonline/books/ascii/docs/9906.htm - 515k - Cached - More pages from this site

4. 1 (PDF)
... RB4, RB5 **high**; RB6, RB7 **low**:PORTB = 0x30;Wait ... a 0, output. **active**If pin is programmed to ... output is **high**. ir (output. disconnected)A **tri-state** buffer (TSB ...
www.ece.msstate.edu/~reese/ece3724/lectures/parport.pdf - 218k - View as html - More pages from this site

5. XR16C872 (PDF)
... After auto or manual **configuration, bits** A0-A2 select ... These are three **state active high** interrupt lines tolRQ12-1 is an **active low tri-state** with an internal ...
www.exar.com/products/16c872.pdf - 322k - View as html - More pages from this site

6. Cafe Andrew 4.0: Tech Info Archives
Cafe Andrew 4.0. December 10, 2002One of the best CSS resources ever. Have you ever wanted to know all the issue accompany IE 5 for the Mac? Ever wondered why your page renders fine in IE 6 XP and looks less than stellar on the M current **state** which may exceed current density requirements for reliable operation. 2. Bus fights on internal **tri** ... **high** temperatures ...
www.cafeandrew.com/archives/cat_tech_info.html - 32k - Cached - More pages from this site

7. FPGA FAQ comp.arch.fpga archives - messages from 3700
... the reset polarity **high** or **low** had >been swapped between ... polarity of the EEPROM to **active low**. I like to ... lies v bidirectional : **tri-state** buffers in the FPGA ...
www.fpga-faq.com/archives/03700.html - 58k - Cached - More pages from this site

8. http://www.htsoft.com/forum/pic/pic.archive.html
PIC C Forum. This forum is for discussion of HI-TECH Software's PIC C compiler. Bug reports and tech support questic fastest response if emailed to support@htsoft.com as well as (or instead of) being posted here. ... programmer complai **configuration bits** were invalid (a Data ... will just access the **high** byte of test - it ... will shift the **high** byte into the **low** the ...
www.htsoft.com/forum/pic/pic.archive.html - 525k - Cached - More pages from this site

9. CS294-8 Final Project Report: Sensor Netorks - Application to Architecture Mapping - by Brett and S
... and a **high** series resistance. Modern capacitors can achieve as much as ~10mJ/mm3 with a **low** ... timer-independe **configuration bits**, such as timer ... generally need **tri-state** buffers.These ...

www-bsac.eecs.berkeley.edu/~warneke/cs294-8/Report.html - 65k - Cached - More pages from this site

10. FPGA FAQ comp.arch.fpga archives - messages from 48450
... Reset is **active low** input pin > > –[KEYBOARD **STATE** MACHINE CONTROL ... 1'; – watch dog reset **high** > kbwa zero watchdog ... for input, output, **and tri-state** control. If you ...
www.fpga-faq.com/archives/48450.html - 80k - Cached - More pages from this site

11. AT40K05AL/10AL/20AL/40AL (PDF)
... the AT40KAL cell. **Configuration bits** for separate muxes and pass ... of a register is **active low** (logic 0) by default level **Tri-state** Output. Leakage Current ...
www.atmel.com/dyn/resources/prod_documents/doc2818.pdf - 571k - View as html - More pages from this site

12. 5K - 50K Gates Coprocessor FPGA with FreeRAM (PDF)
... the AT40K/AT40KLV cell. **Configuration bits** for separate muxes and ... of a register is **active low** (logic 0) by defau AIOZH**High**-level **Tri-state** Output. Leakage Current ...
www.atmel.com/dyn/resources/prod_documents/DOC0896.PDF - 1628k - View as html - More pages from this site

13. Contextual Computing Group: PIC HOWTO
Contextual Computing Group. PIC. The PIC HOWTO. Kent Lyons. Original a HOWTO for Ubicomp Spring 00
www.gvu.gatech.edu/ccg/resources/pic - 22k - Cached - More pages from this site

14. Cafe Andrew 5.0
... device **active** volume and pass ... a very **low** heavy ion ... **high**-current **state** which may exceed current density requ reliable operation. 2. Bus fights on internal **tri** ...
www.cafeandrew.com/archives/000462.html - 15k - Cached - More pages from this site

15. Section 24. Device Configuration (PDF)
© 2003 Microchip Technology Inc.Advance InformationDS70071B-page 24-1MD. ev. i. ceConf. i. gurat. i. o. n24Section signal polarity for the PWM pins. The polarity for the **high** side **and low**. side PWM pins may be selected independently information on these **configuration bits** ...
www.microchip.com/download/lit/pline/dspic/progman/70071b.pdf - 156k - View as html - More pages from this site

16. http://ece.wpi.edu/pit/rowitsch/linux-2.4.21/drivers/net/hp100.c
... is also used for **low** (<1MB) memory (multi-architecture ... we need to remap **high** (>1GB) card memory */ if ... Int an **Tri-State** Int, Bus Master Rd/Wr ...
ece.wpi.edu/pit/rowitsch/linux-2.4.21/drivers/net/hp100.c - 93k - Cached - More pages from this site

17. linux/linux/drivers/net/hp100.c
... is also used for **low** (<1MB) memory (multi-architecture support ... we need to remap **high** (>1GB) card memory */69 **State** Int, Bus Master Rd/Wr, and Mem Map ...
www.cs.fsu.edu/~baker/devices/lxr/http/source/linux/drivers/net/hp100.c - 435k - Cached - More pages from this site

18. http://maine.franklin.ch/Usenet/comp.arch.fpga/20010930_future_Xilinx_products_wish_list
... could be **tri-state** isolated from the ... configuration, and make it **active Low** after configuration ... **state** of these pins to be valid on the **Low** to **High** ...
maine.franklin.ch/Usenet/comp.arch.fpga/20010930_future_Xilinx_product s_wish_... - 77k - Cached - More pages from

19. http://www.fys.ruu.nl/~0307467/scratch/docs/int/new/ports.b
3-0 synchronous transfer period in 25 ns units SeeAlso: #P0502 Bitfields for interrupt condition register: Bit(s) Descriptic P0506) 7 FIFO error interrupt 6 forced interrupt??? ... exists and SEL is **active**, SCSI controller ... not ready **state**! SCS byte is bitmap of supported modes (bit 0 = mode 0, etc.) **high** bytes is bitmap of **active** ...
www.fys.ruu.nl/~0307467/scratch/docs/int/new/ports.b - 351k - Cached - More pages from this site

20. EP0780846
... The terms "**configuration bits**" or "configuration data" are ... provide a **high** output impedance, in a **tri-state** conditic circuit, **high** impedance, **tri-state** output condition.As ...
swpat.ffii.org/pikta/txt/ep/0780/846 - 262k - Cached - More pages from this site

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1	2	(configuration with bit$1) with tri$1state with active with (high and low) and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 07:37
12	2	(processor or flash or ROM or RAM) with (control with bit$1 with logic) with tri$1state with active with (high or low)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 13:52
16	11	"6061780"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 08:00
17	2	(configuration with bit$1) with tri$1state with active with (high and low)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 07:55
21	5	"configuration bits" and (tri-state with active with (high and low))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 07:56
25	16	"5056004"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 08:00
26	6	"5056004" and configuration	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 08:19
28	57	Bowling with Stephen	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 13:51
30	34	(Bowling with Stephen) and (configuration$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 08:22
31	7	(Bowling with Stephen) and PWM	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 08:22
32	7	"6026489"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 09:48
34	11	"5951679"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 09:52
37	25	"5117498"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:10
38	19	"5101484"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:10
39	2	"5740419" and PWM	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:11
40	2	"5826096" and PWM	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:11
42	2	"5938759" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:12

43	2	"5930503" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:13
44	2	"6076154" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:13
45	2	"5706460" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:13
46	2	"5825730" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:13
47	2	"6014723" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:13
48	2	"5790443" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:14
49	2	"5448706" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:14
50	2	"5448703" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:14
51	2	"5206940" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:14
52	2	"4829420" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:15
53	2	"5778416" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:15
54	2	"5600813" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:15
55	2	"5012441" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:15
56	2	"6009454" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:16
57	2	"5828875" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:16
58	2	"4882701" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:16
59	2	"5099445" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:16
60	2	"5379240" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:17

61	2	"5327543" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:22
62	2	"4511990" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:22
63	2	"4829460" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:23
64	2	"5038310" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:23
65	2	"4872128" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:45
66	2	"4977533" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:45
67	2	"5941940" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:45
68	2	"5951627" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:45
69	2	"6058409" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:46
70	2	"5991787" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:46
71	2	"5007020" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:46
72	2	"5197140" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:46
73	2	"5499380" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:46
74	2	"4839846" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:46
75	2	"4941120" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:47
76	2	"6058410" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:47
77	2	"5943249" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:47
78	2	"5212662" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:47

79	2	"6101521" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:47
80	2	"4943940" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:48
81	2	"6134574" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:48
82	2	"5930159" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:48
83	2	"5892697" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:48
84	2	"5619711" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:48
85	2	"5917741" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:49
86	2	"5909385" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:49
87	2	"5276634" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:49
88	2	"5880984" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:49
89	2	"5469377" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:49
90	2	"6115732" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:50
91	2	"5197023" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:50
92	2	"4488252" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:50
93	2	"5715470" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:50
94	2	"4472788" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:50
95	2	"5282153" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:51
96	2	"4782457" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:51

97	2	"4807172" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:51
98	2	"5497340" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:51
99	2	"5774711" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:52
100	2	"5596760" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:52
101	2	"4556938" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:52
102	2	"4398244" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:52
103	2	"5996067" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:52
104	0	"4370248" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:53
105	2	"5689693" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:53
106	2	"5862065" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:53
107	2	"6044392" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:53
108	2	"5892699" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:53
109	2	"5894428" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:54
110	2	"6145049" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:55
111	2	"6128728" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:55
112	2	"5327566" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:55
113	2	"5642516" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:55
114	8	"3781810" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 11:01

115	2	"5463749" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:56
116	2	"5471600" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:56
117	2	"5737570" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:57
118	2	"4481576" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:57
119	2	"4959776" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:57
120	2	"5032986" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:57
121	2	"5155823" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:57
122	2	"6044434" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:58
123	2	"4984213" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:58
124	2	"6058464" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:58
125	2	"6145049" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:58
126	2	"5327566" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:58
127	2	"5642516" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:59
128	2	"6128728" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:59
129	2	"5122981" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:59
130	1	"5812439" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 10:59
131	2	"5696711" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 11:00
132	2	"5808926" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 11:00

133	2	"5694350" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 11:00
134	2	"5568412" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 11:00
135	2	"5764555" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 11:00
136	2	"5748516" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 11:00
137	2	"5548544" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 11:01
146	1		USPAT	2004/09/15 11:47
147	14	"5450027"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 13:39
151	0	"5561384" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 11:51
152	0	"5450027" and (PWM or pulse adj1 width adj1 modulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 11:51
148	11	"5561384"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 13:38
155	2	375/.ccls. and Bowling with Stephen	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 13:51
158	2	(PWM or pulse adj1 width adj1 modulat$3)with (control with bit$1 with logic) with tri$1state with active with (high or low)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 13:54
164	4	(PWM or pulse adj1 width adj1 modulat$3) and ((control with logic) with tri$1state with active with (high or low))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 13:57
167	17	(PWM or pulse adj1 width adj1 modulat$3) and (tri$1state with active with (high or low))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 13:57
170	27	(PWM or pulse adj1 width adj1 modulat$3) and ((control with logic) with tri$1state)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/09/15 13:57